IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Go IWASAKI of Tokyo, JAPAN Serial No: Not assigned Filed: November 25, 2003 For: OUTPUT BUFFER CIRCUIT AND SEMICONDUCTOR MEMORY USING THE SAME								11281 U.S. PTC 10/721310	
Comm P.O. B	top PATENT APPLICATIC issioner for Patents ox 1450 Idria, VA 22313-1450	N .							
Dear S CORR	Sir: ESPONDENCE ADDRES	S: .t	☑ Custo	mer Number 000026	6021				
Transmitted herewith for filing is the patent application identified above.									
	ITEM	TOTAL NO. OF CLAIMS	CULAI	NO. OF CLAIMS OVER BASE	LG/S \$ ENTIT		\$ AMOUNT	\$ FEE	
A	TOTAL CLAIMS FEE	19	-20	0	LG=\$18 SM=\$9	\$18	\$ 0		
В	INDEPENDENT CLAIMS FEE*	5	-3	2	LG=\$86 SM=\$43	\$86	\$172		
c	SUBTOTAL - ADDITIONAL CL	IMS FEE (ADD FINAL COL	UMN IN	LINES A + B)	-1	' -		\$172	
D	MULTIPLE-DEPENDENT CLAI	MS FEE					TY FEE = \$290 TY FEE = \$145	\$ 0	
E	BASIC FEE					ARGE ENTITY FEE = \$770 SMALL ENTITY FEE = \$385 \$ 770.00			
F	TOTAL FILING FEE (ADD TOT	ALS FOR LINES C, D, AND	E)			ALL LIVIT	11122 \$665	\$ 942.00	
G	ASSIGNMENT RECORDING F	Ε					\$ 40	\$ 40	
<u></u>	*LIST INDEPENDENT CLAIMS	1,2,11,13 and 15			ny of this	letterie	onclosed		
 Please charge my Deposit Account No. 50-1314 the amount of \$									
				Respectfully sul HOGAN & HAR	omitted,	Alle			
Date:	November 25, 2003			By: Anthony 1	deler				
500 South Grand Avenue, Suite 1900 Los Angeles, CA 90071 Telephone: (213) 337-6700 Facsimile: (213) 337-6701 Anthony J. Grler Registration No. 41,232 Attorney for Applicant(s)									

Attorney Docket No: 81788.0261

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Go IWASAKI		Examiner:	Not assigned				
Serial No: Not assigned							
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For: OUTPUT BUFFER CIRCUIT AND SEMICON MEMORY USING THE SAME	IDUCTOR						
CERTIFICATE OF MAILING "Express Mail" Mailing Labo Date of Deposit: N	el No. EV 325	214 875 US					
Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450							
Dear Sir:							
I hereby certify that							
two copies of a letter of transmittal							
patent application (<u>43</u> page(s) of spe	ecification; 1	9_ claim(s);	1 page(s) of abstract				
sheet(s) of formal drawings							
certified copy of <u>Japanese</u> patent application No. <u>2002-344926</u> which was file <u>November 28, 2002</u> from which priority is claimed in the subject case pursuant to 3 U.S.C. § 119							
Information Disclosure Statement with _	2_ reference	s					
□ return postcard □							
are being deposited with the United States Postal service with sufficient postage under 37 C.F.R. § 1.10	I Service "Exp 0 on the date i	oress Mail Pondicated above	ost Office to Addressee ve and are addressed to:				
Mail Stop PATENT APPLICA Commissioner for Patents P.O. Box 1450 Arlington, VA 22313-1450	ATION						
Date: November 25, 2003		Reynaldo					
Hogan & Hartson, LLP 500 South Grand Avenue, Suite 1900 Los Angeles, California 90071 Telephone: 213-337-6700 Facsimile: 213-337-6701	Nai	me of person MMM Signa	mailing papers ture				